High Performance Computing

A simple machine model

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To obtain a first machine model, we can group concepts from basic programming as follows:

CPU Calculations, memory operators (e.g. memcpy), branching (if– and for–statements).

Memory Variables, arrays, allocated memory.

IO Terminal in- and output. File access.

The first one is procedural, emphasizing *what* is done.

The last two are data-centered, emphasizing on what is operated.

The connection between these domains, can be visualized as

Hardware architecture is about refining this model and understanding its details.

There is no single truth about hardware architecture; it is descriptive. As hardware varies its architecture might vary.

Understanding hardware architecture will help us to

understand behavior of programs in detail; pick most efficient algorithms and implementations; optimize for specific architectures.

CPU (central processing unit)



In most case, the CPU features at least

the arithmetic and logic unit (ALU), the floating point unit (FPU), and the control unit (CU).

The arithmetic and logic unit performs:

arithmetic operations: +, -, *, / on integers; logic operations: +(OR), *(AND) on boolean.

The floating point unit performs:

basic arithmetic operations: +, -, *, / of float and double; advanced arithmetic operations: sqrt, exp, sin, cos, etc.

The control unit performs operations whose meaning we discuss later. For completeness, it

fetches, decodes, preprocesses, and executes instructions; loads and stores data from memory and cache.



Memory stores and provides instructions and data.

Often "memory" refers to system memory (RAM), but there is also memory, called cache, that is associated closer to the CPU.

On modern systems areas in memory storing instructions and data may not overlap.

Recall that 1Byte = 8Bit. The following units are commonly used:

1KiB = 1024Byte, 1kB = 1KB = 1000byte, 1MiB = 1024KiB, 1MB = 1000KB, 1GiB = 1024MiB, 1GB = 1000MB, 1TiB = 1024GiB, 1TB = 1000GB.



The input/output system covers:

screen (terminal), keyboards, printers; disks (hard disk, solid state disk, magnet tape); network.



The connection between the CPU and memory is called the memory bus.

The connection between the memory and IO is an artifact of the simple model that we use.

Note that disks are attached indirectly to the CPU, insinuating that access is slower.